

## AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

### Listing of Claims:

1. – 10. (canceled)

11. (currently amended) ~~The system of claim 10;~~ A modular system for connecting multiple voice and data communications devices located at a customer's premises to a T1 network, comprising:

a system chassis having a system backplane for receiving and connecting one or more system modules to the T1 network; and

one or more system modules removably connected to the system chassis for connecting one or more voice and data communications devices to the T1 network,

wherein the one or more system modules include dumb access modules, smart access modules, or a combination of both dumb and smart access modules, and

wherein the one or more system modules include any combination of one or more of the following access modules:

an FXS/FXO access module, wherein the FXS/FXO access module includes an automatic gain control circuit;

a fractional T1 port;

a Nx56/64 access module;

a U-BRITE access module; or

a DDS access module.

12. (original) The system of claim 11, wherein the bank controller unit maintains performance information regarding the T1 network.

13. (original) The system of claim 12, wherein the bank controller unit supports the following T1 formats:

Extended Superframe;

Superframe;

TR-08 with alarm-16; and

TR-08 with alarm-13.

14. (original) The system of claim 13, wherein the bank controller unit provides network and fractional T1 control, access module control, alarm control, user interface control, and backplane control.

15. (original) The system of claim 14, wherein the bank controller unit includes a field programmable logic array for providing clock generation, chip select, and backplane interface functions.

16. (original) The system of claim 15, wherein the field programmable logic array is further used to interface the bank controller unit with each access module using a serial peripheral interface located on the system backplane, provide timing generation in local, loop, and external timing modes, provide dialtone and ringback tone generation, multiplex backplane data with fractional T1 data, generate bit clock and byte clock data, and generate chip selects for the T1 interface transceiver,

fractional T1 interface transceiver, and front panel LEDs and switches located on the system chassis.

17. (original) The system of claim 16, wherein the access modules include

a bus transceiver connected to a module signaling bus for interfacing input/output ports on the access modules with a backplane bus on the system backplane; and

a bus control logic unit coupled to a control bus portion of the backplane for interfacing control signals generated by a processor on the bank controller unit with processor-controlled circuit components on each access module;

18. (original) The system of claim 17, wherein:

each access module is operable to communicate a module type unique identification code to the bank controller unit, and

the bank controller unit is responsive to the module type identification code and automatically configures the system to provide access to the network T1 and allocate bandwidth to the customer premises device connected to each access module.

19. (original) The system of claim 18, wherein the bank controller unit controls signaling modes, transmit attenuation, receive attenuation, and 2-wire line impedance for each access module.

20. (original) The system of claim 19, wherein the bank controller unit communicates with smart access modules to determine access module type, timeslot, configuration, status, and test information for the smart access modules.

21. – 24. (canceled)

25. (currently amended) ~~The system of claim 24,~~

A modular system for providing a functional connection between multiple customer premises communication devices and a T1 network data service terminating at the customer premises, the system comprising:

a. a system chassis having a bank of module slots, each slot adapted for receiving one of a plurality of removable system modules such that the system modules can be inserted in and removed from the module slots from a front portion of the chassis;

b. the system chassis further comprising a system backplane adapted for electrically connecting to the system module connectors on a rear portion of the system modules;

c. the system modules including a plurality of access modules operable to provide a functional interface between the T1 service and different types of the customer premises communications devices, including both voice and data devices, the access modules used in the system being selected by the customer to correspond to the types of customer premises communications devices to be connected to the network;

d. a power service unit connected to the system for supplying power received from the network T1 to the one or more system modules; and

e. a bank controller unit connected to the system chassis for controlling the power service unit and the one or more system modules,

wherein the one or more system modules include dumb access modules, smart access modules, or a combination of both dumb and smart access modules, and

wherein the bank controller unit provides network and fractional T1 control, access module control, alarm control, user interface control, and backplane control.

26. (original) The system of claim 25, wherein the bank controller unit includes a field programmable logic array for providing clock generation, chip select, and backplane interface functions.

27. (original) The system of claim 26, wherein the field programmable logic array is further operable to interface the bank controller unit with each access module using a serial peripheral interface located on the system backplane, provide timing generation in local, loop, and external timing modes, provide dialtone and ringback tone generation, multiplex backplane data with fractional T1 data, generate bit clock and byte clock data, and generate chip selects for the T1 interface transceiver, fractional T1 interface transceiver, and front panel LEDs and switches located on the system chassis.

28. (original) The system of claim 27, wherein the access modules include  
a bus transceiver connected to a module signaling bus for interfacing input/output ports on the access modules with a backplane bus on the system backplane; and

a bus control logic unit coupled to a control bus portion of the backplane for interfacing control signals generated by a processor on the bank controller unit with processor-controlled circuit components on each access module;

29. (original) The system of claim 28, wherein:

each access module is operable to communicate a module identification code to the bank controller unit, and

the bank controller unit is responsive to the module identification code to automatically configure the system to provide access to the network T1 and allocate bandwidth to the customer premises device connected to each access module.

30. (original) The system of claim 29, wherein the bank controller unit controls signaling modes, transmit attenuation, receive attenuation, and 2-wire line impedance for each access module.

31. (original) The system of claim 30, wherein the bank controller unit communicates with smart access modules to determine access module type, timeslot, configuration, status, and test information for the smart access modules.